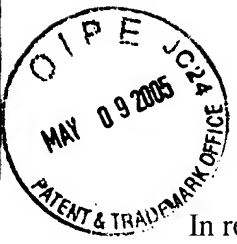


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IR-2076 (2-2689)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Kohji ANDOH et al.

Date: May 5, 2005

Serial No.: 10/728,482

Group Art Unit: 2812

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Examiner: A. Stevenson

For: PROCESS FOR PREPARATION OF SEMICONDUCTOR WAFER SURFACE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

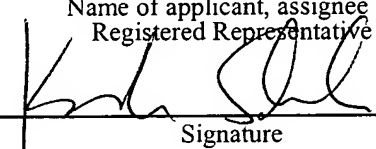
RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT

In response to the Notice of Non-compliant Amendment dated April 22, 2005, a corrected copy of the Listing Of The Claims Section of Applicant's communication filed April 14, 2005 is attached, in which the status of all of the claims is indicated.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450, on May 5, 2005


Respectfully submitted,

Kourosh Salehi

Name of applicant, assignee or
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